



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: N. HIGASHI et al.

Serial No.: Not yet informed. Group No.

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Examiner:

for: METHOD FOR INSPECTION OF CIRCUIT BOARDS AND APPARATUS FOR
INSPECTION OF CIRCUIT BOARDS

Commissioner of Patents and Trademarks
Washington, D.C. 20231

VERIFICATION OF A TRANSLATION

I, the below named translator, hereby declare that:
My name and post office address are as stated below.
I am knowledgeable in the English language and in the Japanese
language.

I believe the attached English translation to be a
true and complete literal translation of the 45 page Japanese
language patent application text with 3 sheets of drawings
entitled "METHOD FOR INSPECTION OF CIRCUIT BOARDS AND APPARATUS
FOR INSPECTION OF CIRCUIT BOARDS" by Noboru HIGASHI, Daisuke
NAGAI and Kenichi KAIDA. This Japanese language document was
filed in the United States Patent and Trademark Office on July
26, 2001.

I hereby declare that all statements made herein of
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Date: September 20, 2001

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